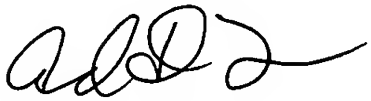
disclosed in the specification). The present amendment intends to clarify references to trademarks of Cypress MicroSystems, Inc. and others (see, e.g., M.P.E.P. § 608.01(v)). No new matter is introduced.

REMARKS

Claims 1-41 are presented for consideration in the present application, which is now believed to be in condition for examination. Early notice to that effect is earnestly solicited.

Respectfully submitted,

WAGNER, MURABITO & HAO LLP



Anthony C. Murabito
Registration No. 35,295

Andrew D. Fortney, Ph.D.
Registration No. 34,600

Two North Market Street
Third Floor
San Jose, California 95113
(408) 938-9060
ADF/adf

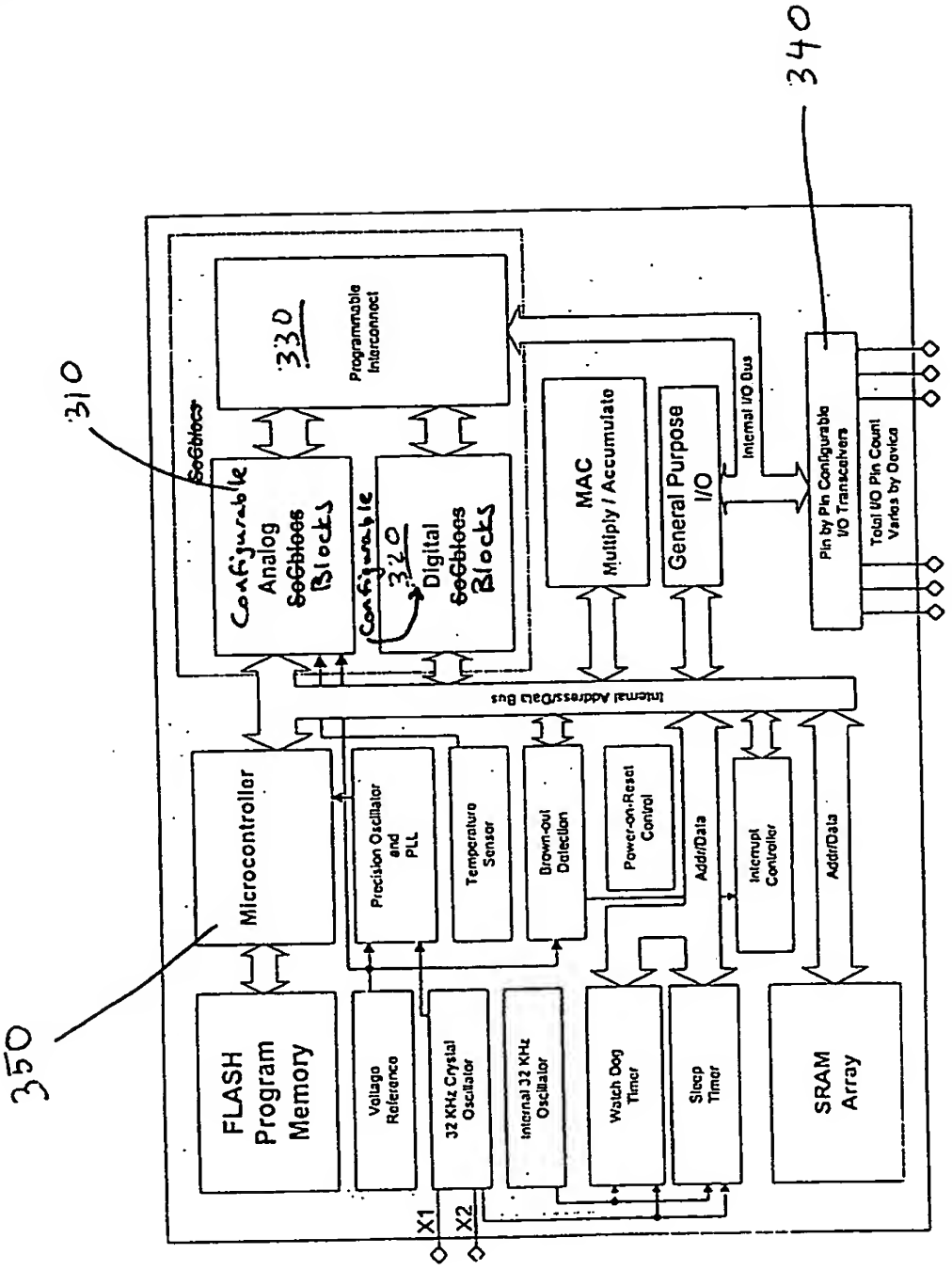


Figure 3

400

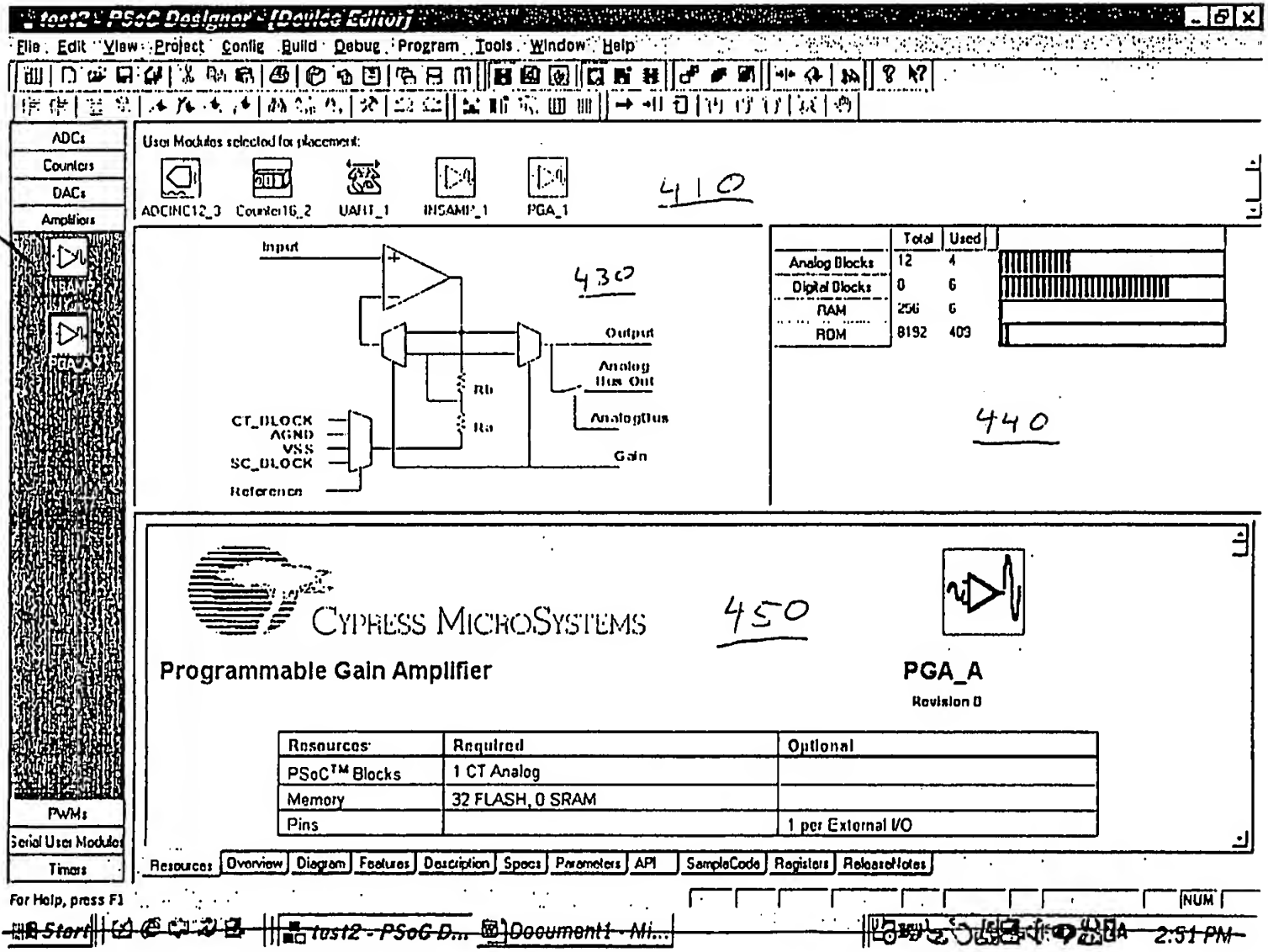
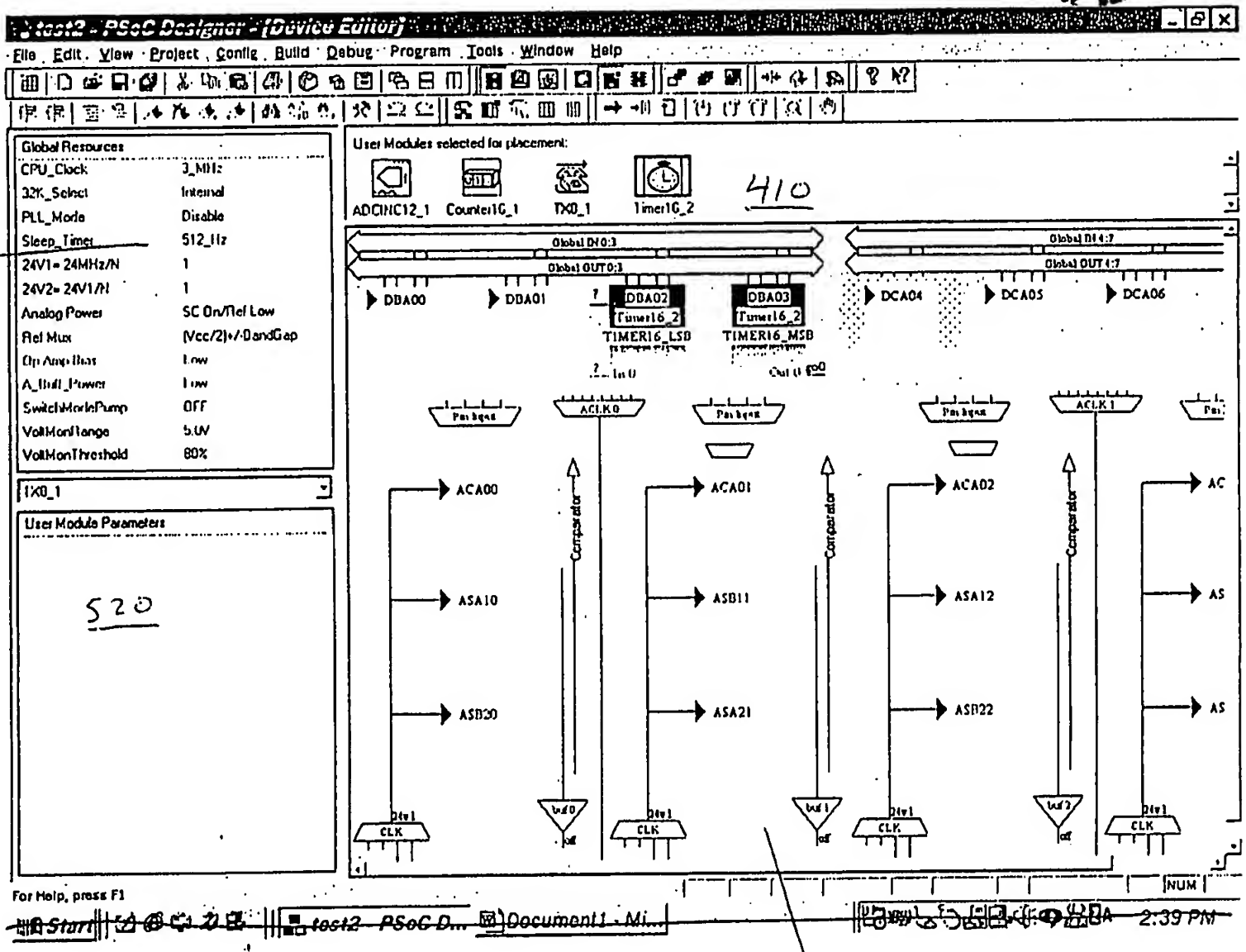


FIGURE 4

500



530

520

410

510

FIGURE 5

700

```
<!--Project DB Ver 0.03 08/10/00-->
<PSOC_DEVICE_DB>
<USER_MODULE_LIST>
    <!--Variable Resolution Incremental ADC-->
<USER_MODULE NAME="ADCEXP" TYPE="PSOC_ADC" HTML="ADCEXP.HTM" ICON="ADC.ico" METAFILE="ADCEXP.emf"
API_PATH_TYPE="CUSTOM">
<SHAPE SHAPE_TYPE="BLOCKLIST">
<BLOCK_LIST>
<BLOCK NAME="ADC" TYPE="ANALOG_SC">
<REGISTER_LIST>
<REGISTER NAME="CR0">
<BITFIELD_LIST>
<BITFIELD NAME="OSZ" VALUE="OSZ_32"/>
<BITFIELD NAME="CLOCK_PHASE" VALUE="PHASE_NORM"/>
<BITFIELD NAME="SIGN" VALUE="POS"/>
<BITFIELD NAME="CAP_SIZE" VALUE="16"/>
</BITFIELD_LIST>
</REGISTER>
<REGISTER NAME="CR1">
<BITFIELD_LIST>
<BITFIELD NAME="CAP_SIZE" VALUE="0"/>
</BITFIELD_LIST>
</REGISTER>
<REGISTER NAME="CR2">
<BITFIELD_LIST>
<BITFIELD NAME="A_OUT" VALUE="DISABLED"/>
<BITFIELD NAME="COMP_OUT" VALUE="ENABLED"/>
<BITFIELD NAME="AZ_PHASE" VALUE="AZ_PH1"/>
```

FIGURE 7A

```
        <API_REGISTER_ALIAS NAME="CounterMSBSL" SOURCE="CNT_MSB" REGISTER_NAME="DIG_Input"/>
        <API_REGISTER_ALIAS NAME="CounterMSBOS" SOURCE="CNT_MSB" REGISTER_NAME="DIG_Output"/>
        <API_REGISTER_ALIAS NAME="CounterMSBDR0" SOURCE="CNT_MSB" REGISTER_NAME="DATA_0"/>
        <API_REGISTER_ALIAS NAME="CounterMSBDR1" SOURCE="CNT_MSB" REGISTER_NAME="DATA_1"/>
        <API_REGISTER_ALIAS NAME="CounterMSBDR2" SOURCE="CNT_MSB" REGISTER_NAME="DATA_2"/>
        <API_REGISTER_ALIAS NAME="CounterMSBCR0" SOURCE="CNT_MSB" REGISTER_NAME="CONTROL_0"/>
        <API_REGISTER_ALIAS NAME="CounterLSBFN" SOURCE="CNT_LSB" REGISTER_NAME="DIG_BasicFunction"/>
        <API_REGISTER_ALIAS NAME="CounterLSBSL" SOURCE="CNT_LSB" REGISTER_NAME="DIG_Input"/>
        <API_REGISTER_ALIAS NAME="CounterLSBOS" SOURCE="CNT_LSB" REGISTER_NAME="DIG_Output"/>
        <API_REGISTER_ALIAS NAME="CounterLSBDR0" SOURCE="CNT_LSB" REGISTER_NAME="DATA_0"/>
        <API_REGISTER_ALIAS NAME="CounterLSBDR1" SOURCE="CNT_LSB" REGISTER_NAME="DATA_1"/>
        <API_REGISTER_ALIAS NAME="CounterLSBDR2" SOURCE="CNT_LSB" REGISTER_NAME="DATA_2"/>
        <API_REGISTER_ALIAS NAME="CounterLSBCR0" SOURCE="CNT_LSB" REGISTER_NAME="CONTROL_0"/>
        <API_REGISTER_ALIAS NAME="TimerFN" SOURCE="TMR" REGISTER_NAME="DIG_BasicFunction"/>
        <API_REGISTER_ALIAS NAME="TimerSL" SOURCE="TMR" REGISTER_NAME="DIG_Input"/>
        <API_REGISTER_ALIAS NAME="TimerOS" SOURCE="TMR" REGISTER_NAME="DIG_Output"/>
        <API_REGISTER_ALIAS NAME="TimerDR0" SOURCE="TMR" REGISTER_NAME="DATA_0"/>
        <API_REGISTER_ALIAS NAME="TimerDR1" SOURCE="TMR" REGISTER_NAME="DATA_1"/>
        <API_REGISTER_ALIAS NAME="TimerDR2" SOURCE="TMR" REGISTER_NAME="DATA_2"/>
        <API_REGISTER_ALIAS NAME="TimerCR0" SOURCE="TMR" REGISTER_NAME="CONTROL_0"/>
        <API_REGISTER_ALIAS NAME="Comp_Ctrl" SOURCE="AnalogComp" TYPE="RESERVED_RESOURCE"
REGISTER_NAME="AnalogComparatorControl"/>
      </API_REGISTER_ALIAS_LIST>
    </USER_MODULE>
  </USER_MODULE_LIST>
</PSOC_DEVICE_DB>
```


FIGURE 7E